

**• General Description**

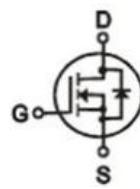
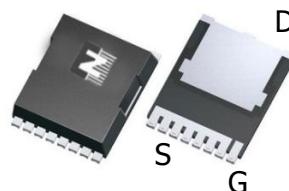
It combines advanced trench MOSFET technology with a low resistance package to provide extremely low $R_{DS(ON)}$.

• Features

- Advance device construction
- Low $R_{DS(ON)}$ to minimize conduction loss
- Low Gate Charge for fast switching
- Low Thermal resistance

• Application

- Synchronous Rectification for AC-DC/DC-DC converter
- Oring switches
- Power Tools

• Product Summary $V_{DS}=80V$ $R_{DS(ON)}=1.6m\Omega$ $I_D=240A$ 

TOLL

• Ordering Information:

Part NO.	ZMS015N08HR
Marking	ZMS015N08
Packing Information	REEL TAPE
Basic ordering unit (pcs)	2000

• Absolute Maximum Ratings ($T_C = 25^\circ C$)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current	$I_D@T_C=25^\circ C$	240	A
	$I_D@T_C=75^\circ C$	182	A
	$I_D@T_C=100^\circ C$	151	A
Pulsed Drain Current ^①	I_{DM}	720	A
Diode continuous forward current	I_S	175	A
Diode pulse current	$I_{S,pulse}$	525	A
Total Power Dissipation	$P_D@T_C=25^\circ C$	290	W
Operating Junction Temperature	T_J	-55 to 150	°C
Storage Temperature	T_{STG}	-55 to 150	°C
Single Pulse Avalanche Energy@L=0.1mH	E_{AS}	480	mJ



•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R _{thJC}	-	-	0.43	°C/W
Thermal resistance, junction - ambient	R _{thJA}	-	-	40	°C/W
Soldering temperature, wavesoldering for 10s	T _{sold}	-	-	265	°C

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} =0V, I _D =250uA	80			V
Gate Threshold Voltage	V _{GS(TH)}	V _{GS} =V _{DS} , I _D =250uA	2		4	V
Drain-Source Leakage Current	I _{DSS}	V _{DS} =80V, V _{GS} =0V			1.0	uA
Gate- Source Leakage Current	I _{GSS}	V _{GS} =±20V, V _{DS} =0V			±100	nA
Static Drain-source On Resistance	R _{DS(ON)}	V _{GS} =10V, I _D =30A		1.6	2.1	mΩ
Forward Transconductance	g _{FS}	V _{DS} =10V, I _D =40A		28		s
Source-drain voltage	V _{SD}	I _S =100A			1.28	V

•Electronic Characteristics

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Input capacitance	C _{iss}	f = 1MHz V _{DS} =25V	-	9130	-	pF
Output capacitance	C _{oss}		-	6640	-	
Reverse transfer capacitance	C _{rss}		-	117	-	

•Gate Charge characteristics(T_a = 25°C)

Parameter	Symbol	Condition	Min.	Typ	Max.	Unit
Gate Resistance	R _g	f = 1MHz		2.5		Ω
Total gate charge	Q _g	V _{DD} =30V I _D = 30A V _{GS} = 10V	-	121	-	nC
Gate - Source charge	Q _{gs}		-	24	-	
Gate - Drain charge	Q _{gd}		-	23	-	
Turn-On Time	t _{on}	V _{GS} =10V, V _{DS} =15V R _G =3.3Ω, I _D =25A		112		ns
Turn-ON Delay time	t _{D(on)}			41		
Turn-ON Rise time	t _r			65		ns
Turn-Off Delay time	t _{D(off)}			130		ns



Turn-Off Fall time	t_f		34		ns
Reverse Recovery Time	t_{RR}	VDD = 20 V, $dI/dt = 100 A/s$, IS = 30 A	85		ns
Charge Time	t_a		34		ns
Discharge Time	t_b		28		ns
Reverse Recovery Charge	Q_{RR}		120		nC

Note: ① Pulse Test : Pulse width $\leq 10\mu s$, Duty cycle $\leq 1\%$;

Fig.1 Gate-Charge Characteristics

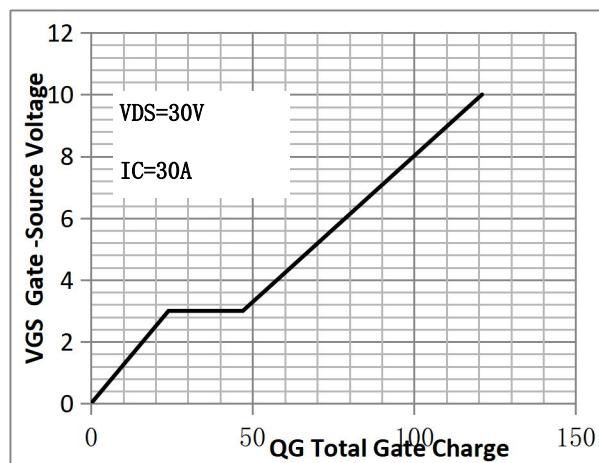


Fig.2 Capacitance Characteristics

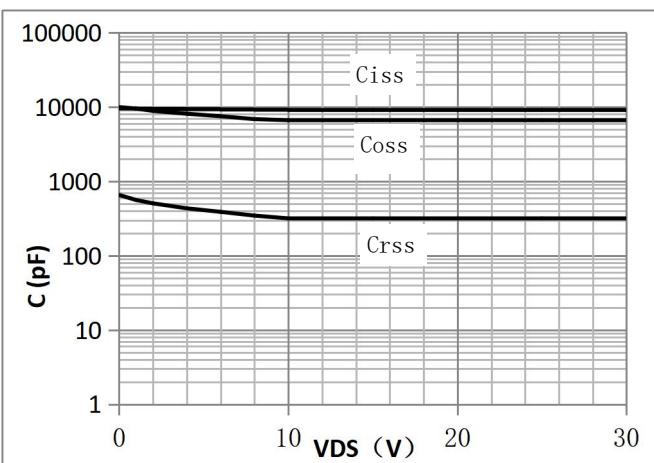


Fig.3 Power Dissipation

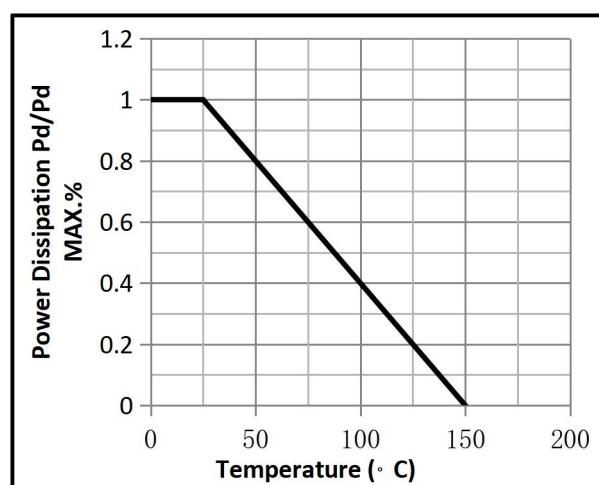


Fig.4 Typical output Characteristics

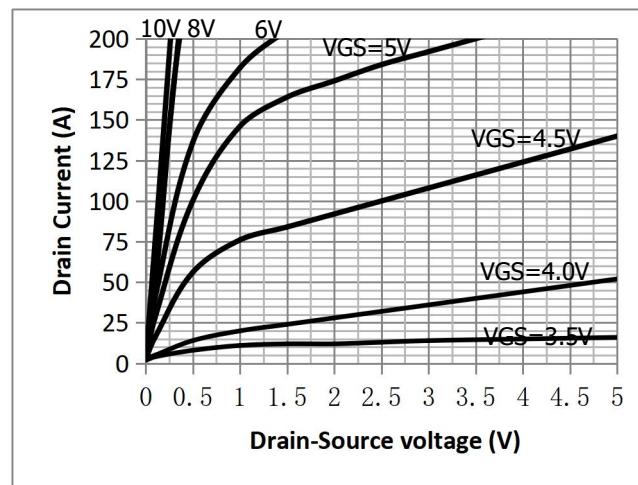




Fig.5 Threshold Voltage V.S Junction Temperature

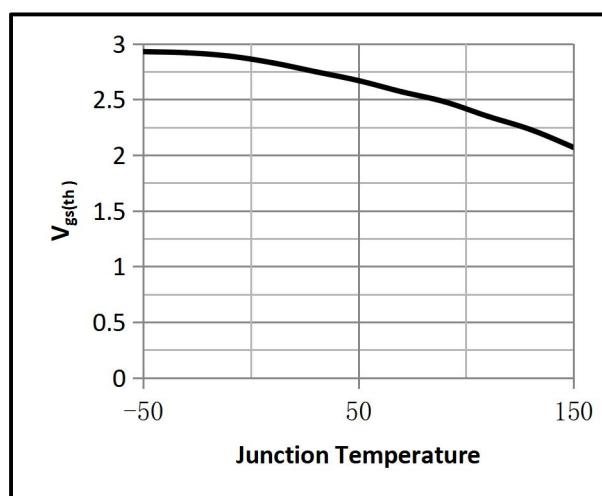


Fig.6 Resistance V.S Drain Current

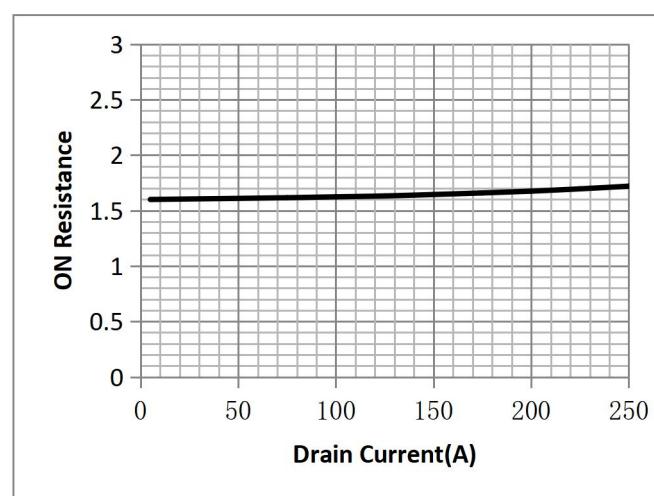


Fig.7 On-Resistance VS Gate Source Voltage

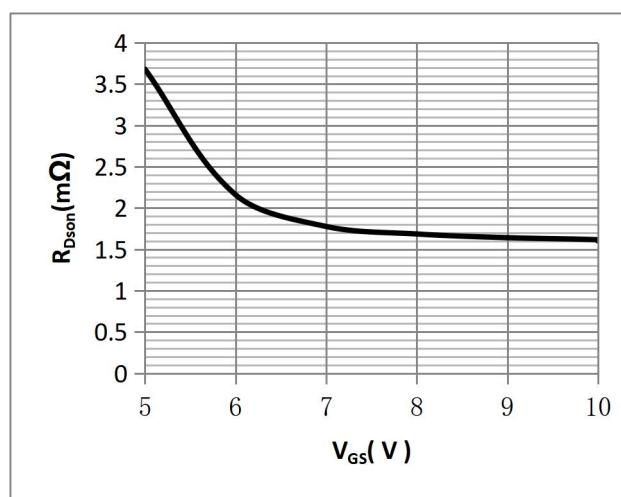


Fig.8 On-Resistance V.S Junction Temperature

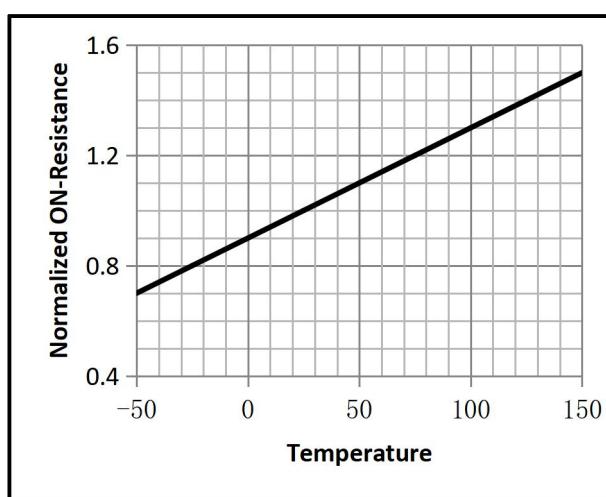


Fig.9 SOA Maximum Safe Operating Area

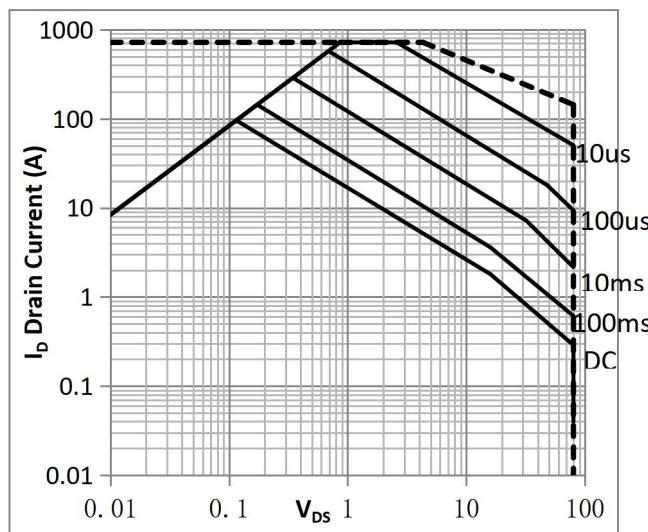


Fig.10 ID-Junction Temperature

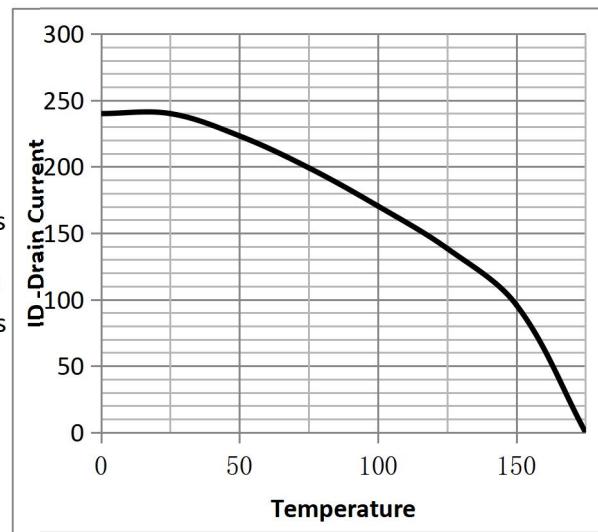




Figure.11 Diode Forward Voltage vs. Current

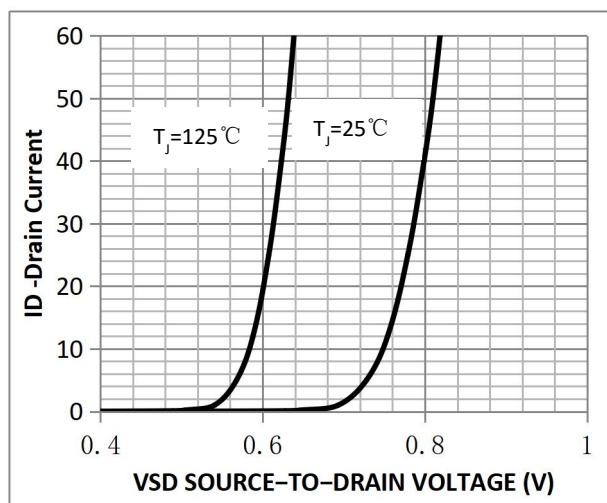


Figure.12 Transfer Characteristics

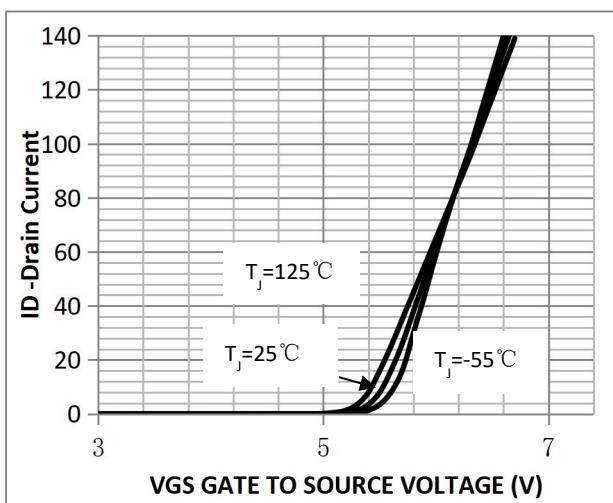


Fig.13 Switching Time Measurement Circuit

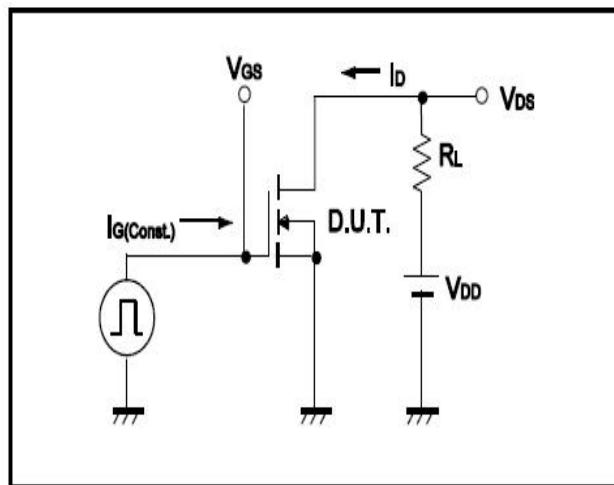


Fig.14 Gate Charge Waveform

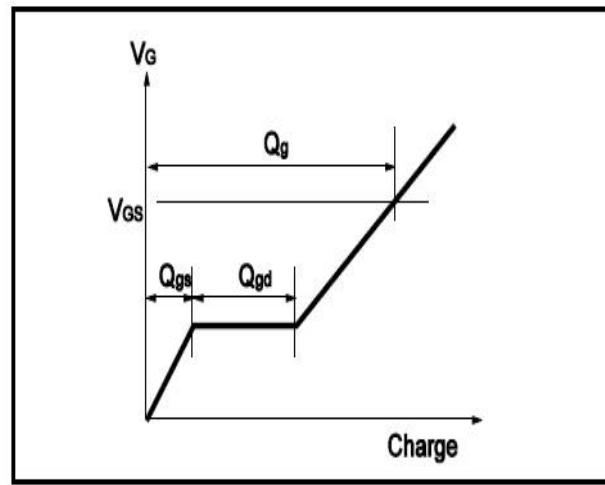


Fig.15 Switching Time Measurement Circuit

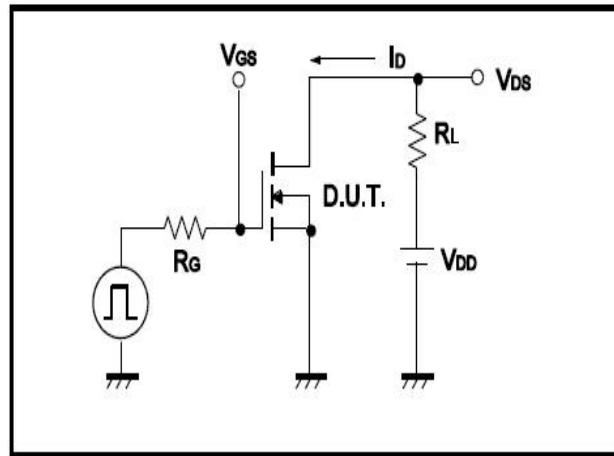
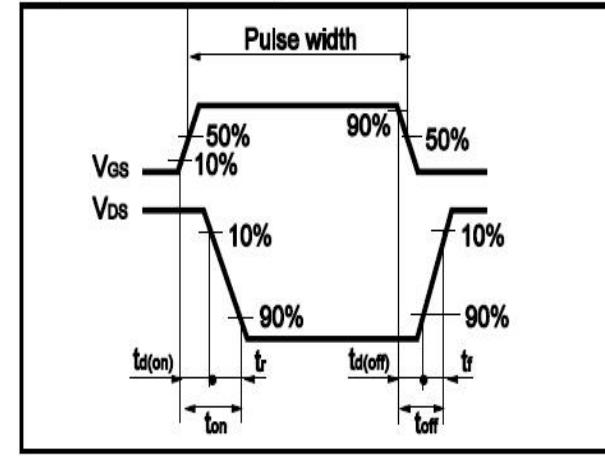


Fig.16 Gate Charge Waveform





•Dimensions (TOLL)

Unit: mm

